

**Product Summary**

$V_{DS}$	1200 V
$I_D (T_C=25^\circ\text{C})$	64 A
$R_{DS(on),typ}$	40 mΩ@ $V_{GS}=18\text{V}$

**Features**

- Low On-Resistance with High Blocking Voltage
- Low Capacitance
- Avalanche Ruggedness
- Halogen Free, RoHS Compliant

**Benefits**

- High Frequency Operation
- Enabling Higher Switching Frequency
- Increased Power Density
- Reduction of Heat Sink Requirements

**Applications**

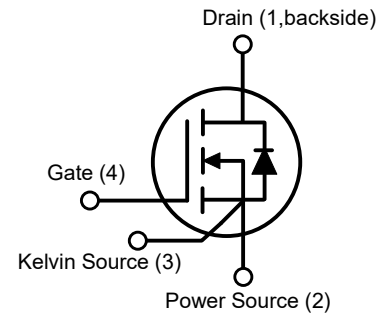
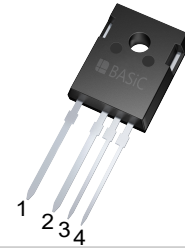
- Switch Mode Power Supplies (SMPS)
- Power Inverter & Solar Inverter
- Motor Drivers & EV Charging Station
- DC/DC Converter

**Package Pin Definitions**

- Pin1 and backside - Drain
- Pin2 - Power Source
- Pin3 - Kelvin Source
- Pin4 - Gate

**Package Parameters**

Part Number	Marking	Package
B3M040120ZN	B3M040120ZN	TO-247-4NL

**Package: TO-247-4NL**


**Maximum Ratings**

Symbol	Parameter	Test conditions	Value	Unit
$V_{DSmax}$	Drain-Source Voltage	$V_{GS}=0V, I_D=100\mu A$	1200	V
$V_{GSmax}$	Gate-Source Voltage		-10/22	V
$V_{GSop}$	Recommended Gate-Source Voltage		-5/18	V
$I_D$	Continuous Drain Current	$V_{GS}=18V, T_C=25^\circ C$	64	A
		$V_{GS}=18V, T_C=100^\circ C$	45	A
$I_{D,pulse}$	Pulsed Drain Current	Pulse with $t_p$ limited by $T_{jmax}$	124	A
$P_{tot}$	Power Dissipation	$T_C=25^\circ C, T_j=175^\circ C$	312	W
$T_j$	Operating Junction Temperature		-55~175	$^\circ C$
$T_{stg}$	Storage Temperature		-55~175	$^\circ C$
$M_d$	TO-247 mounting torque	M3 Screw	0.7	N·m

**Electrical Characteristics (Defined at  $T_j=25^\circ C$  unless otherwise specified)**
**Static Characteristics**

Symbol	Parameter	Test conditions	Value			Unit
			Min.	Typ.	Max.	
$V_{(BR)DSS}$	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_D=100\mu A$	1200			V
$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS}=V_{DS}, I_D=10mA$ (tested after 1ms pulse at $V_{GS}=20V$ )	2.3	2.7	3.5	V
		$V_{GS}=V_{DS}, I_D=10mA, T_j=175^\circ C$ (tested after 1ms pulse at $V_{GS}=20V$ )		1.9		
$I_{GSS}$	Gate-Source Leakage Current	$V_{GS}=18V, V_{DS}=0V$			100	nA
$I_{DSS}$	Zero Gate Voltage Drain Current	$V_{DS}=1200V, V_{GS}=0V$		1	50	$\mu A$
		$V_{DS}=1200V, V_{GS}=0V, T_j=175^\circ C$		10	200	
$R_{DS(on)}$	Drain-Source On-State Resistance	$V_{GS}=18V, I_D=40A$		40	55	m $\Omega$
		$V_{GS}=18V, I_D=40A, T_j=175^\circ C$		75		
		$V_{GS}=15V, I_D=40A$		50		
$g_{fs}$	Transconductance	$V_{DS}=10V, I_D=40A$		16		S

**Thermal Characteristics**

Symbol	Parameter	Value			Unit
		Min.	Typ.	Max.	
$R_{th(jc)}$	Thermal Resistance from Junction to Case		0.48	0.70	K/W

**AC Characteristics**

Symbol	Parameter	Test conditions	Value			Unit
			Min.	Typ.	Max.	
$C_{iss}$	Input Capacitance	$V_{GS}=0V, V_{DS}=800V$ $f=100kHz, V_{AC}=25mV$		1870		pF
$C_{oss}$	Output Capacitance			82		pF
$C_{rss}$	Reverse Transfer Capacitance			6		pF
$E_{oss}$	$C_{oss}$ Stored Energy			33		$\mu J$
$C_{O(ER)}$	Effective Output Capacitance, Energy Related	$V_{GS}=0V, 0V < V_{DS} < 800V$		105		pF
$C_{O(TR)}$	Effective Output Capacitance, Time Related	$V_{GS}=0V, 0V < V_{DS} < 800V$		157		pF
$R_{G(int)}$	Internal Gate Resistance	$f=1MHz, V_{AC}=25mV$		1.3		$\Omega$

**Gate Charge Characteristics**

Symbol	Parameter	Test conditions	Value			Unit
			Min.	Typ.	Max.	
$Q_{GS}$	Gate to Source Charge	$V_{DS}=800V$ $I_D=40A$ $V_{GS}=-5/+18V$		28		nC
$Q_{GD}$	Gate to Drain Charge			46		nC
$Q_G$	Total Gate Charge			88		nC

**Switching Characteristics**

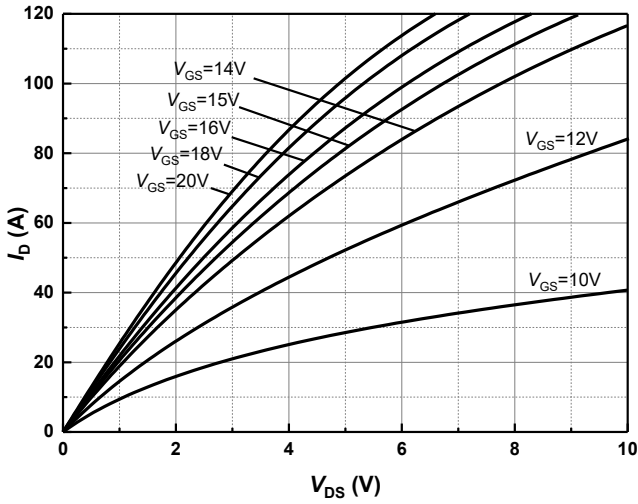
Symbol	Parameter	Test conditions	Value			Unit
			Min.	Typ.	Max.	
$t_{d(on)}$	Turn-On Delay Time			11		ns
$t_r$	Rise Time	$V_{DC}=800V, V_{GS}=-5/18V$ $I_D=40A, R_{G(ext)}=8.2\Omega$		31		ns
$t_{d(off)}$	Turn-Off Delay Time	$L_\sigma=50nH, T_j=25^\circ C$ FWD <sup>1)</sup> : body diode at $V_{GS}=-5V$		33		ns
$t_f$	Fall Time	Inductive Load Eon includes diode reverse recovery		10		ns
$E_{on}$	Turn-On Energy (FWD=Body Diode)			650		$\mu J$
$E_{off}$	Turn-Off Energy (FWD=Body Diode)			190		$\mu J$
$E_{on}$	Turn-On Energy (FWD=SiC SBD)	$V_{DC}=800V, V_{GS}=-5/18V$ $I_D=40A, R_{G(ext)}=8.2\Omega$		570		$\mu J$
$E_{off}$	Turn-Off Energy (FWD=SiC SBD)	$L_\sigma=50nH, T_j=25^\circ C$ FWD <sup>1)</sup> : B3D20120H		190		$\mu J$
$t_{d(on)}$	Turn-On Delay Time			8		ns
$t_r$	Rise Time	$V_{DC}=800V, V_{GS}=-5/18V$ $I_D=40A, R_{G(ext)}=8.2\Omega$		31		ns
$t_{d(off)}$	Turn-Off Delay Time	$L_\sigma=50nH, T_j=175^\circ C$ FWD <sup>1)</sup> : body diode at $V_{GS}=-5V$		40		ns
$t_f$	Fall Time	Inductive Load Eon includes diode reverse recovery		9		ns
$E_{on}$	Turn-On Energy (FWD=Body Diode)			890		$\mu J$
$E_{off}$	Turn-Off Energy (FWD=Body Diode)			190		$\mu J$
$E_{on}$	Turn-On Energy (FWD=SiC SBD)	$V_{DC}=800V, V_{GS}=-5/18V$ $I_D=40A, R_{G(ext)}=8.2\Omega$		440		$\mu J$
$E_{off}$	Turn-Off Energy (FWD=SiC SBD)	$L_\sigma=50nH, T_j=175^\circ C$ FWD <sup>1)</sup> : B3D20120H		190		$\mu J$

1) Note: FWD: Freewheeling diode

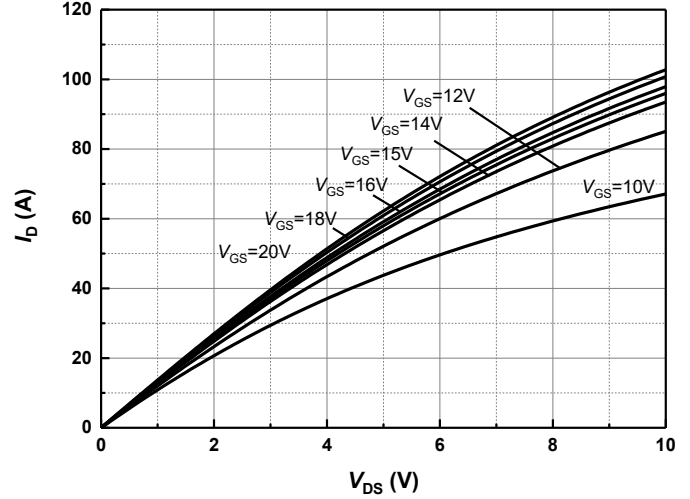
**Reverse Diode Characteristics**

Symbol	Parameter	Test conditions	Value			Unit
			Min.	Typ.	Max.	
$V_{SD}$	Diode Forward Voltage	$V_{GS}=-5V, I_{SD}=20, T_j=25^{\circ}C$		4.9		V
		$V_{GS}=-5V, I_{SD}=20A, T_j=175^{\circ}C$		4.1		
$I_{SD}$	Continuous Diode Forward Current	$V_{GS}=-5V, T_c=25^{\circ}C$		50		A
$I_{SD,pulse}$	Pulsed Diode Current	$V_{GS}=-5V$ , pulse width $t_p$ limited by $T_{jmax}$		125		A
$t_{rr}$	Reverse Recovery Time	$V_{GS}=-5V, V_{DC}=800V, I_{SD}=40A$ $-di_F/dt=2800A/\mu s$ $T_j=25^{\circ}C$		27		ns
$Q_{rr}$	Reverse Recovery Charge			230		nC
$I_{rrm}$	Peak Reverse Recovery Current			18		A
$t_{rr}$	Reverse Recovery Time	$V_{GS}=-5V, V_{DC}=800V, I_{SD}=40A$ $-di_F/dt=3500A/\mu s$ $T_j=175^{\circ}C$		29		ns
$Q_{rr}$	Reverse Recovery Charge			840		nC
$I_{rrm}$	Peak Reverse Recovery Current			40		A

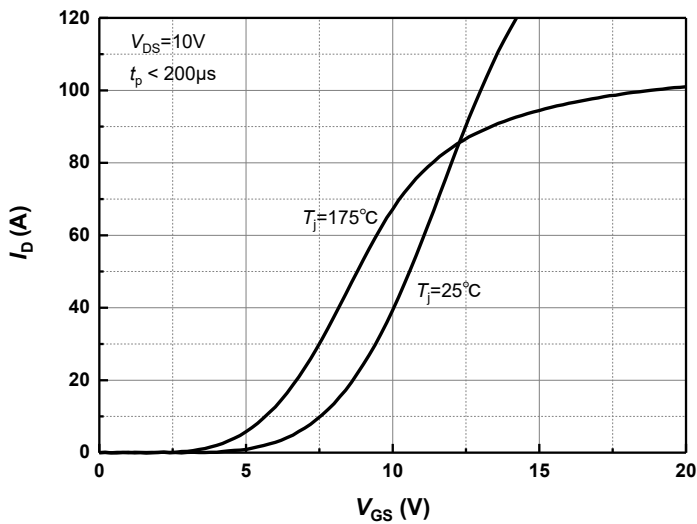
**Typical Performance**



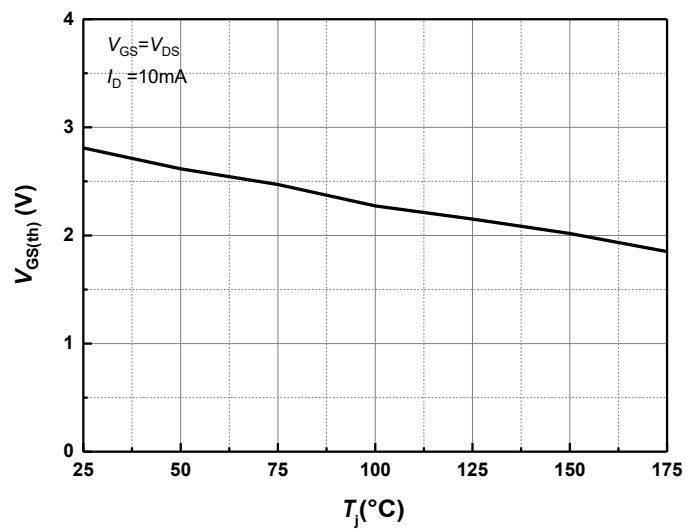
**Figure 1** Typical Forward Output Characteristics at  $T_j = 25^\circ\text{C}$



**Figure 2** Typical Forward Output Characteristics at  $T_j = 175^\circ\text{C}$

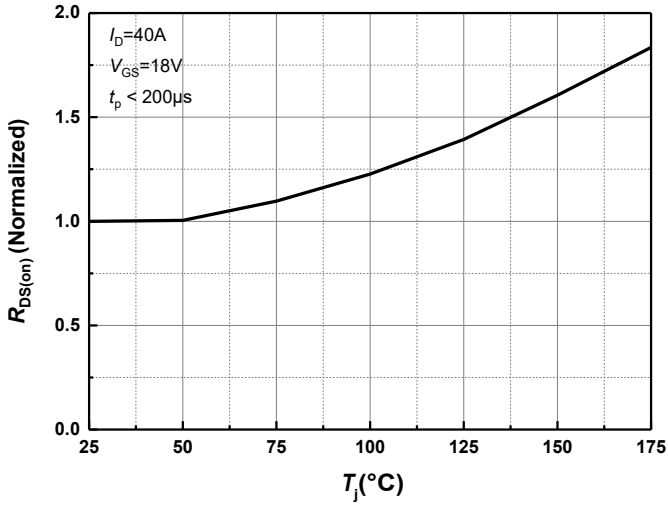


**Figure 3** Transfer Characteristics for Various Temperature

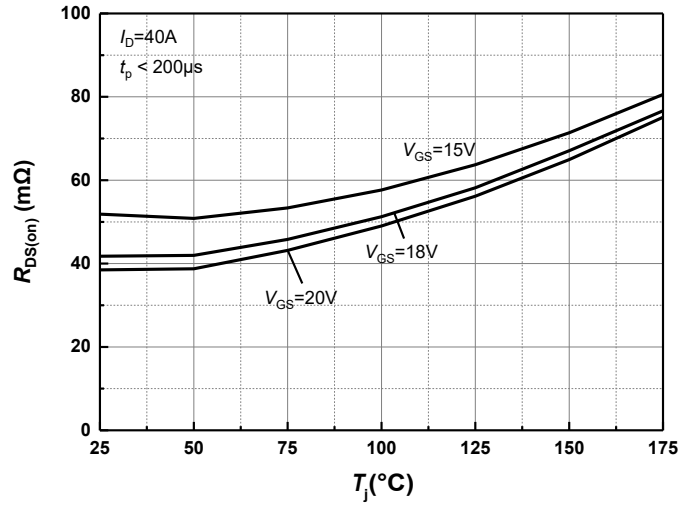


**Figure 4** Threshold Voltage for Various Temperature

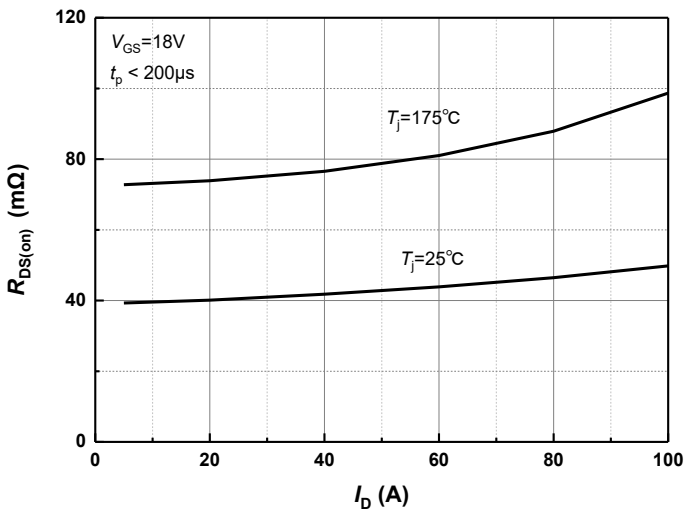
**Typical Performance**



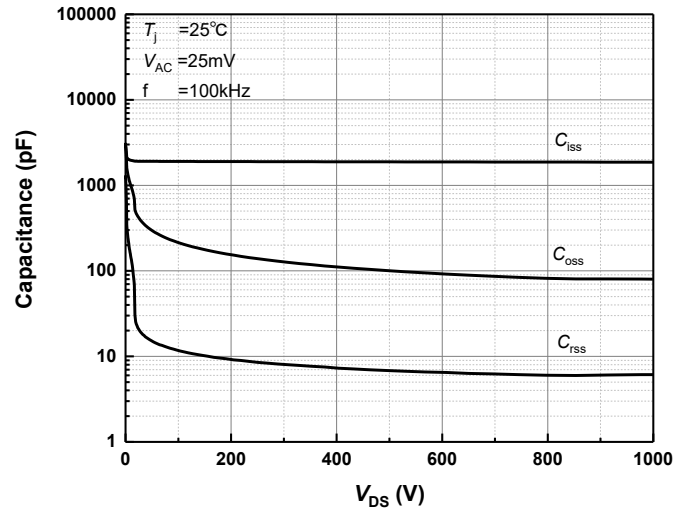
**Figure 5 Normalized On-Resistance for Various Temperature**



**Figure 6 On-Resistance vs. Temperature for Various Gate-Source Voltage**

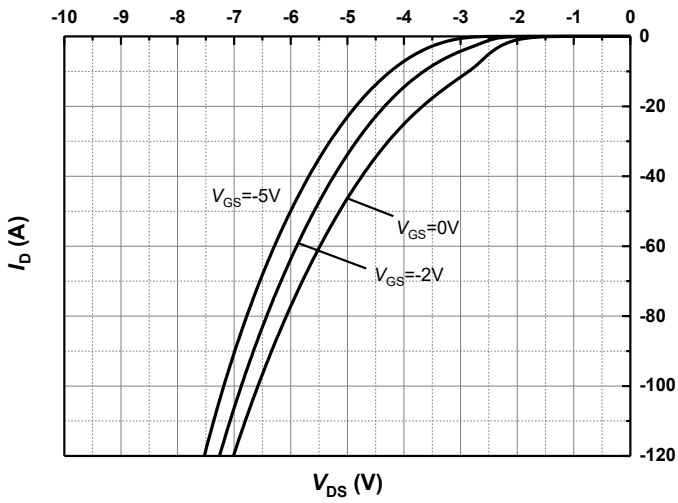


**Figure 7 On-Resistance vs. Drain Current for Various Temperature**

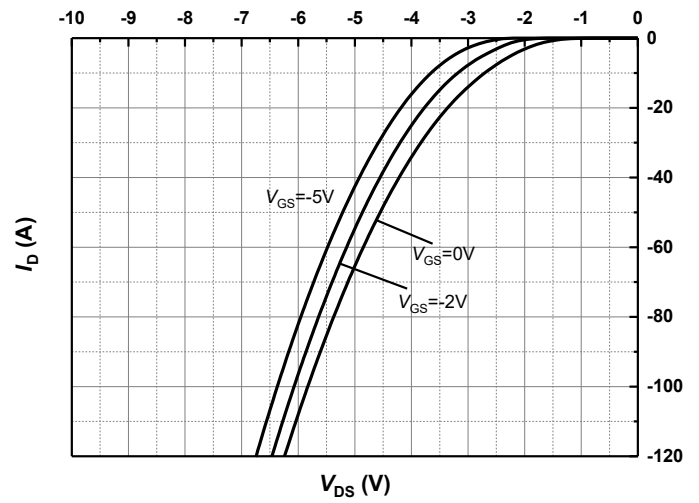


**Figure 8 Capacitance vs. Drain-Source Voltage (0 - 1000V)**

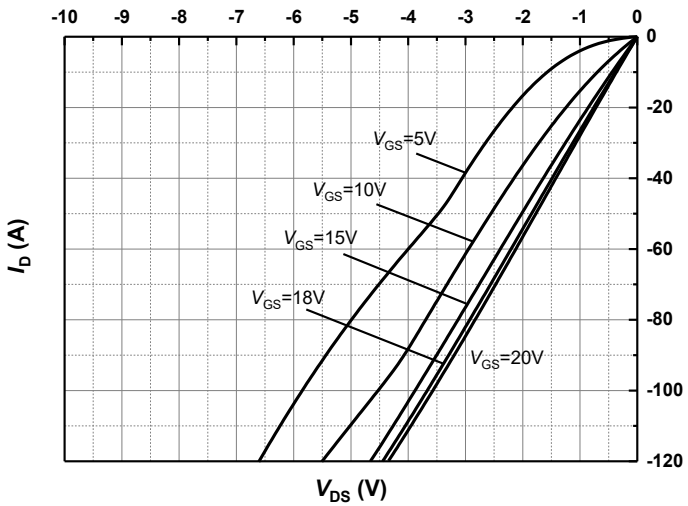
**Typical Performance**



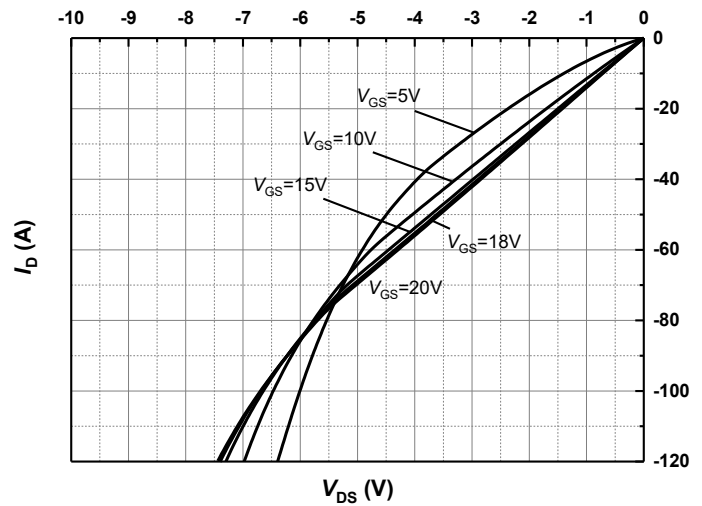
**Figure 9** Body Diode Characteristics at  $T_j=25^{\circ}\text{C}$



**Figure 10** Body Diode Characteristics at  $T_j=175^{\circ}\text{C}$



**Figure 11** 3rd Quadrant Characteristics at  $T_j=25^{\circ}\text{C}$



**Figure 12** 3rd Quadrant Characteristics at  $T_j=175^{\circ}\text{C}$

Typical Performance

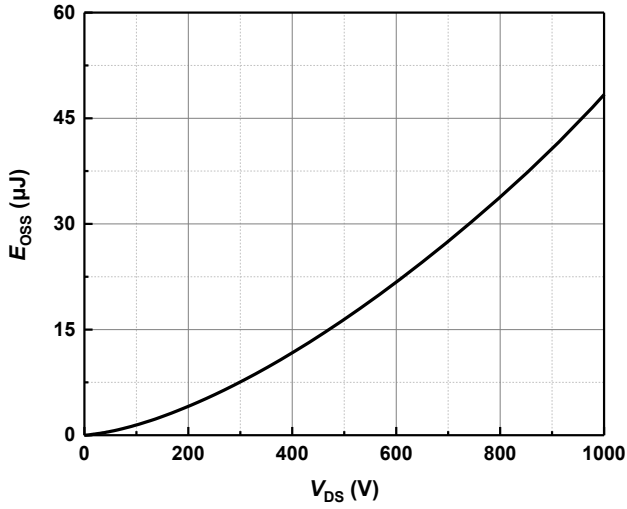


Figure 13 Output Capacitor stored Energy

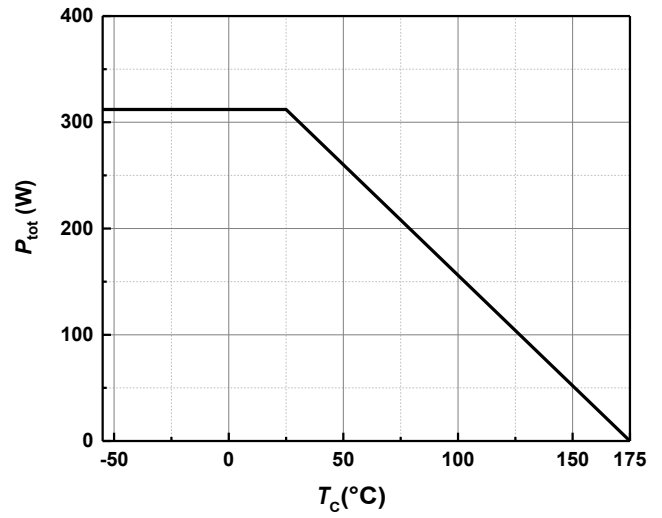


Figure 14 Maximum Power Dissipation Derating vs. Case Temperature

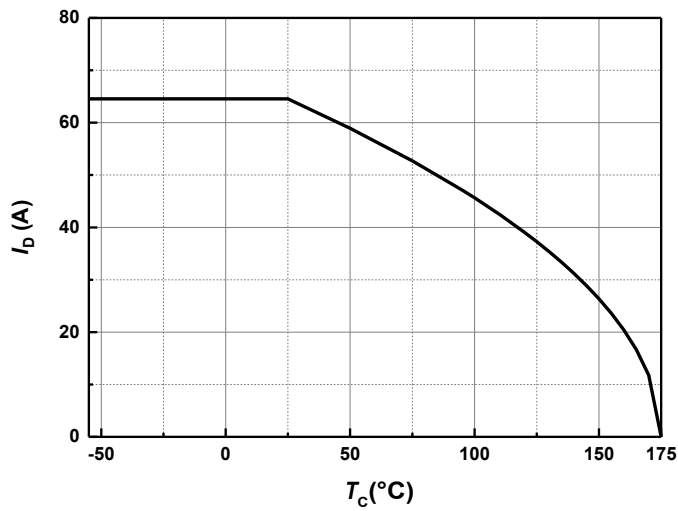


Figure 15 Continuous Drain Current Derating vs. Case Temperature

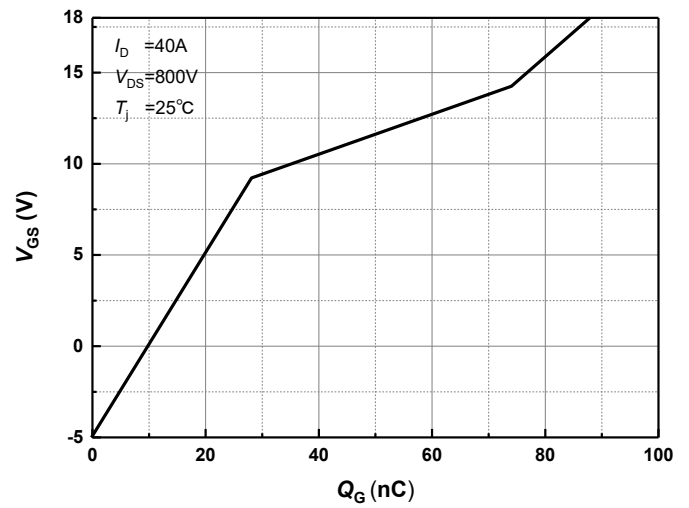
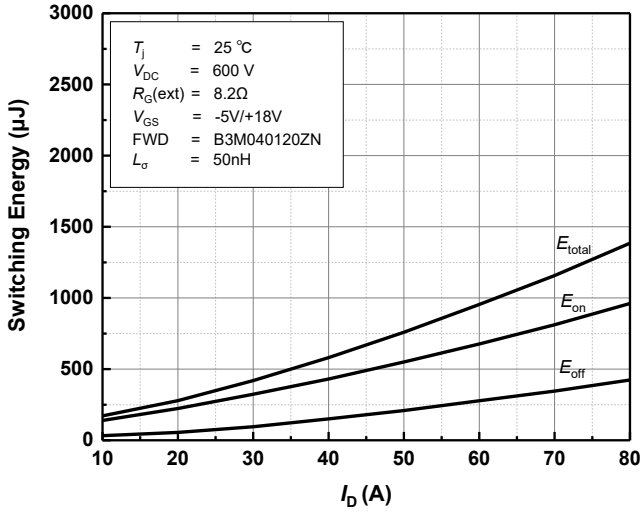
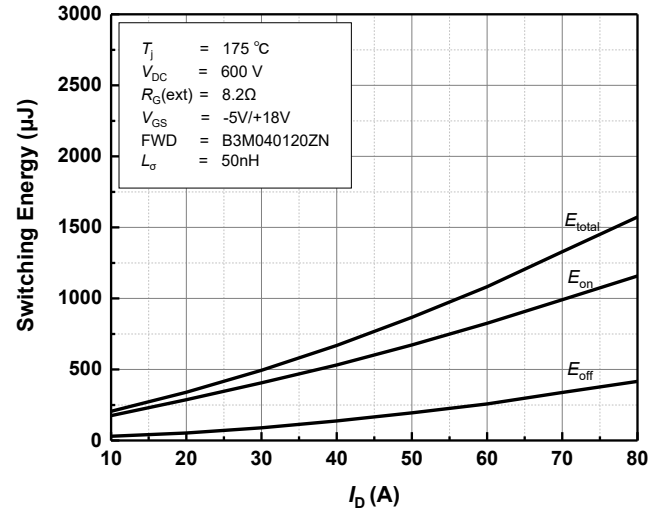


Figure 16 Gate Charge Characteristics

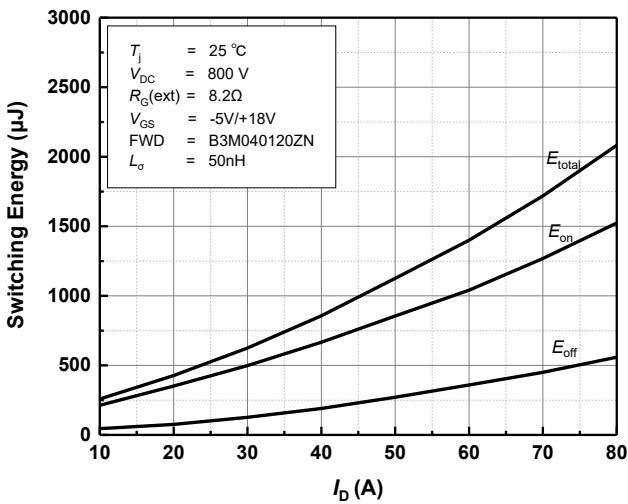
**Typical Performance**



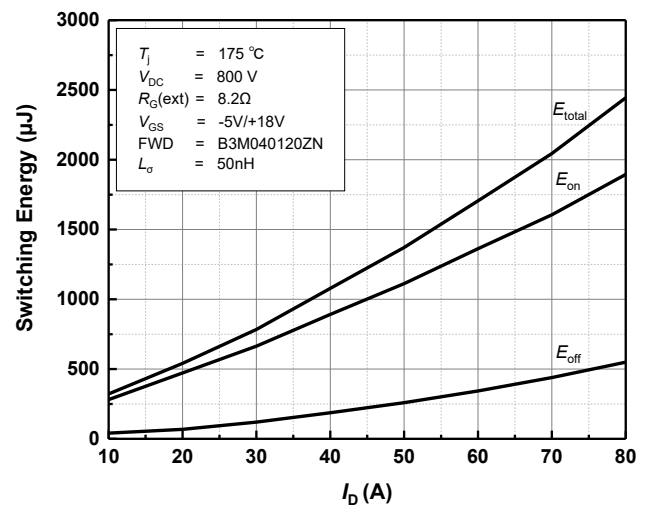
**Figure 17 Clamped Inductive Switching Energy vs. Drain Current ( $V_{DC} = 600\text{V}$ ) at  $T_j = 25^\circ\text{C}$**



**Figure 18 Clamped Inductive Switching Energy vs. Drain Current ( $V_{DC} = 600\text{V}$ ) at  $T_j = 175^\circ\text{C}$**

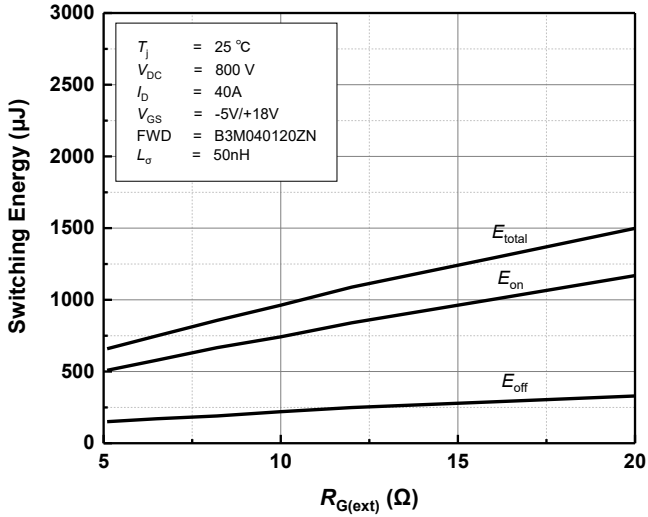


**Figure 19 Clamped Inductive Switching Energy vs. Drain Current ( $V_{DC} = 800\text{V}$ ) at  $T_j = 25^\circ\text{C}$**

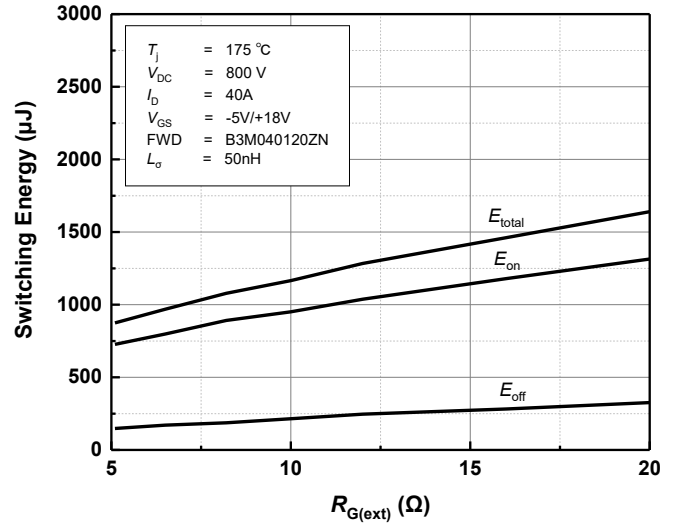


**Figure 20 Clamped Inductive Switching Energy vs. Drain Current ( $V_{DC} = 800\text{V}$ ) at  $T_j = 175^\circ\text{C}$**

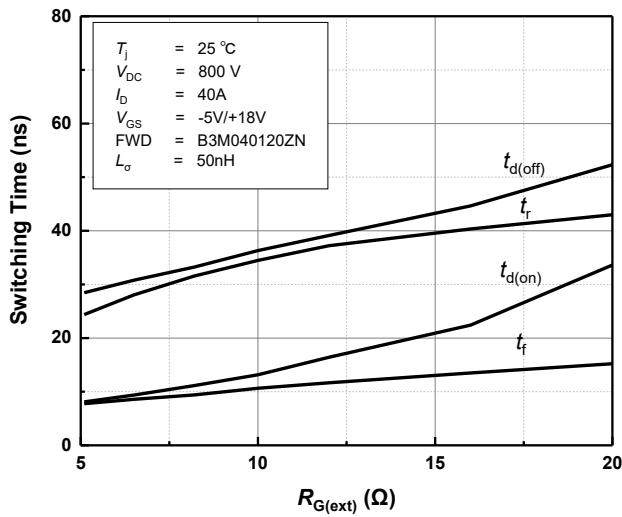
**Typical Performance**



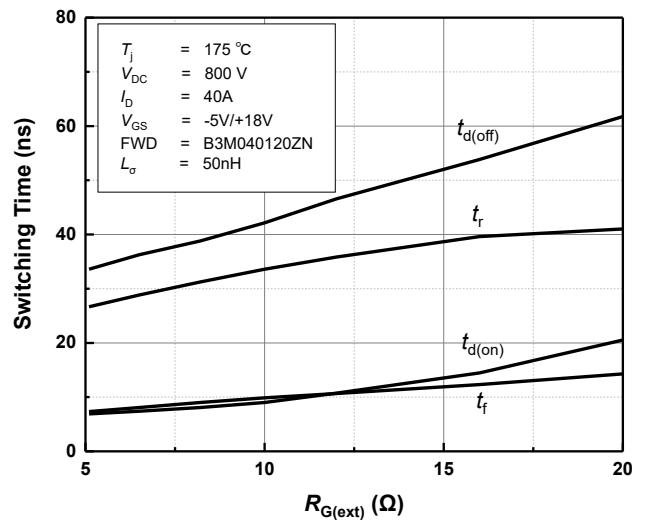
**Figure 21 Clamped Inductive Switching Energy vs. External Gate Resistance at  $T_j=25^\circ\text{C}$**



**Figure 22 Clamped Inductive Switching Energy vs. External Gate Resistance at  $T_j=175^\circ\text{C}$**

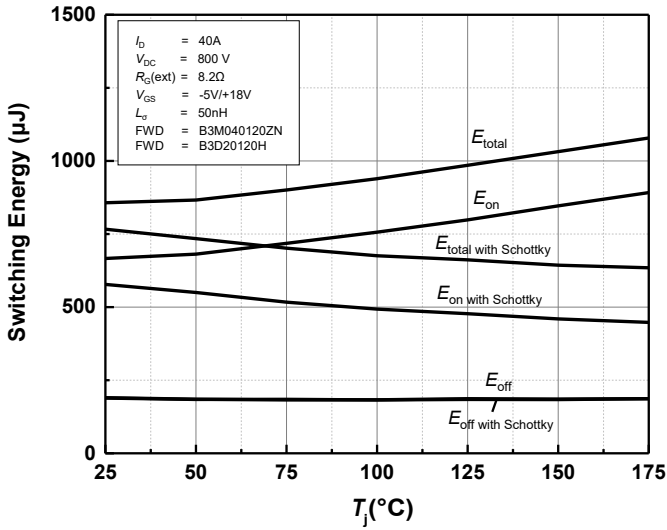


**Figure 23 Clamped Inductive Switching Time vs. External Gate Resistance at  $T_j=25^\circ\text{C}$**

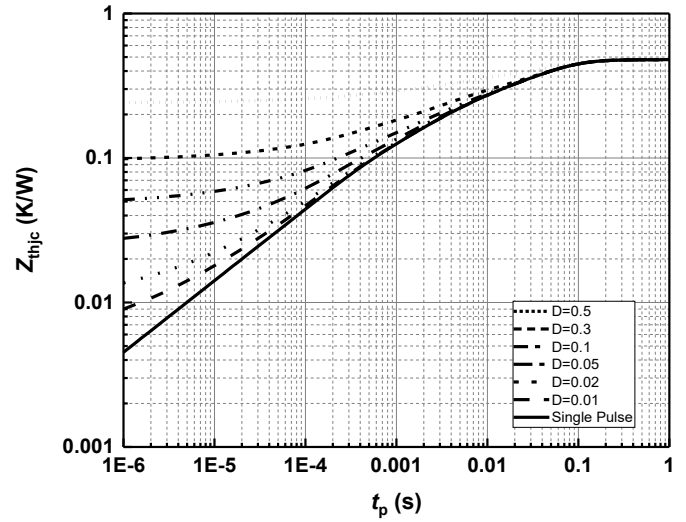


**Figure 24 Clamped Inductive Switching Time vs. External Gate Resistance at  $T_j=175^\circ\text{C}$**

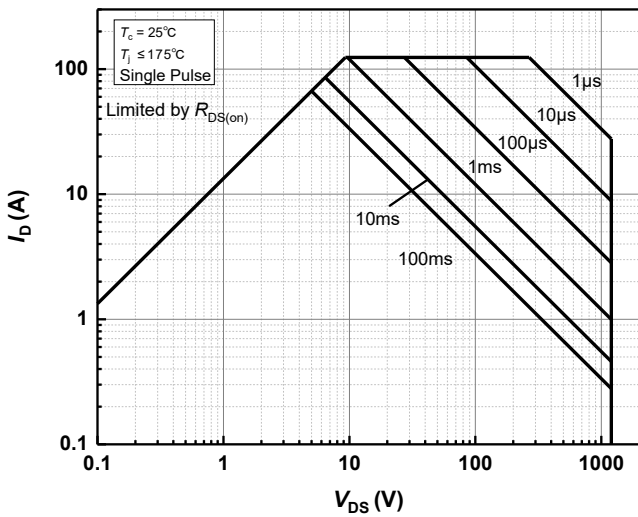
**Typical Performance**



**Figure 25 Clamped Inductive Switching Energy vs. Temperature**

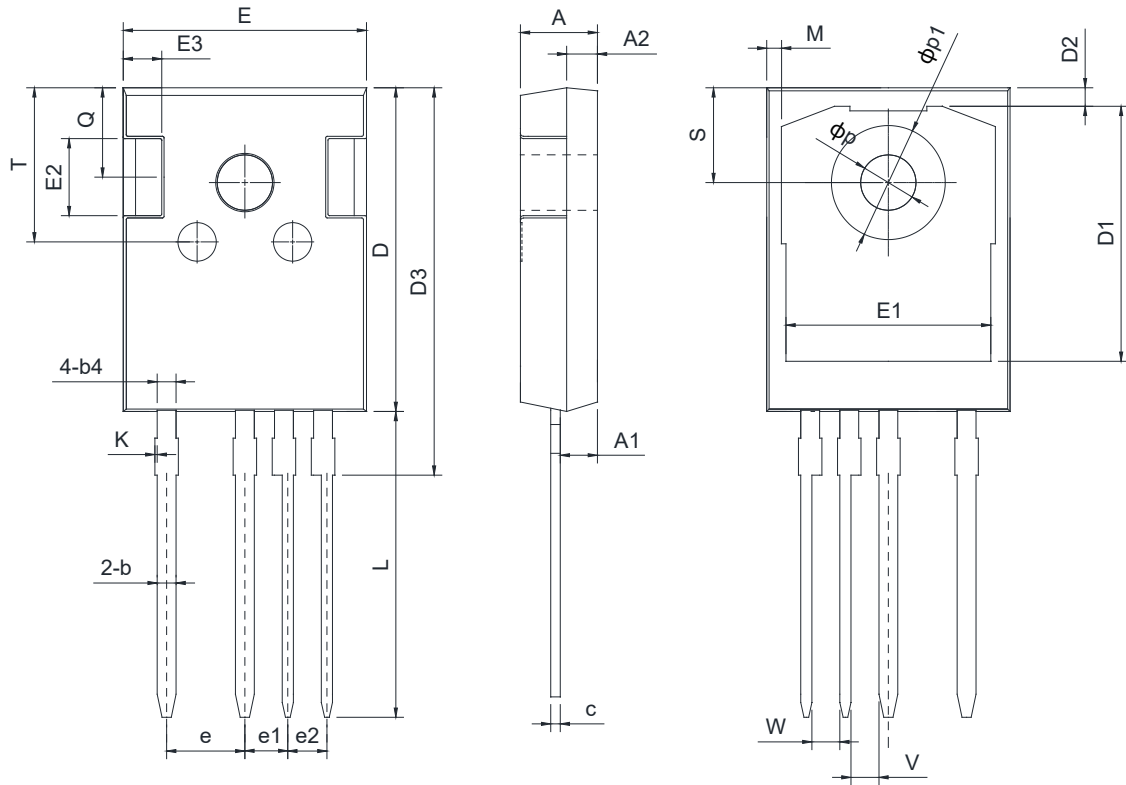


**Figure 26 Transient Thermal Impedance (Junction - Case)**



**Figure 27 Forward Biased Safe Operating Area**

**Package Dimensions**



SYMBOL	mm		
	MIN	NOM	MAX
A	4.90	5.00	5.10
A1	2.31	2.41	2.51
A2	1.90	2.00	2.10
b	1.16	-	1.29
b4	1.16	-	1.29
c	0.59	-	0.66
D	20.90	21.00	21.10
D1	16.25	16.55	16.85
D2	1.05	1.20	1.35
D3	24.97	25.12	25.27
E	15.70	15.80	15.90
E1	13.10	13.30	13.50
E2	4.90	5.00	5.10
E3	2.40	2.50	2.60
e	4.98	5.08	5.18
e1	2.69	2.79	2.89
e2	2.44	2.54	2.64
K	0.00	-	0.20
L	19.80	19.92	20.10
φP	3.50	3.60	3.70
φP1	-	-	7.40
Q	5.60	-	6.00
S	6.00	6.15	6.30
T	9.80	-	10.20
U	6.00	-	6.40
V	1.44	1.84	2.24
W	1.44	1.84	2.24

**Revision History**

<b>Document Version</b>	<b>Date of Release</b>	<b>Description of Changes</b>
Rev. 0.0	2025-08-20	Draft datasheet created.
Rev. 0.1	2025-12-06	Update.

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